

Syllabus : 5CS8A SYSTEM DESIGNS in UML LAB

Class: V Sem. B.Tech.

Evaluation Branch: Computer Engg.

Schedule per Week Practical Hrs : 3

Examination Time = Four (4) Hours

Maximum Marks = 75 [Sessional/Mid-term (45) & End-term (30)]

1. The students shall be able to use following modules of UML for system description, implementation and finally for product development.

- Capture a business process model.
- The User Interaction or Use Case Model
 - describes the boundary and interaction between the system and users. Corresponds in some respects to a requirements model.
- The Interaction or Communication Model
 - describes how objects in the system will interact with each other to get work done.
- The State or Dynamic Model
 - State charts describe the states or conditions that classes assume over time. Activity graphs describe the workflows the system will implement.
- The Logical or Class Model
 - describes the classes and objects that will make up the system.
- The Physical Component Model
 - describes the software (and sometimes hardware components) that make up the system.
- The Physical Deployment Model
 - describes the physical architecture and the deployment of components on that hardware architecture. The students are expected to use the UML models, prepare necessary documents using UML and implement a system. Some hardware products like digital clock, digital camera, washing machine controller, air conditioner controller, an electronic fan regulator, an elementary mobile phone etc. may also be chosen. The students shall be assigned one problem on software based systems and another involving software as well as hardware.

Syllabus Covered(Fonts in bold format red color have covered)

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